SANTHIRAM ENGINEERING COLLEGE :: NANDYAL

(Autonomous)

EXAMINATION BRANCH

MTECH II SEM (R23) Autonomous Supplementary Examinations Timetable-November-2025
(2023 & 2024 Batch's)

FN: 10:00 AM to 01:00 PM

Date/Day	VLSI System Design	Embedded Systems	Computer Science and Engineering	Computer Science and Engineering (AI&ML)
09-11-2026 Monday	CMOS Mixed Signal IC Design **(23D57201)**	Advanced IOT and its Applications **(23D55202)**	Linux Administration **(23D58202)**	Introduction To Large Language Models **(23D13212)**
11-11-2026 Wednesday	Physical Design Automation **(23D57202)**	Arm Microcontroller Architecture and Programming **(23D55201)**	Deep Learning **(23D58201)**	
13-11-2026 Friday	Semiconductor Memory Design & Testing **(23D5204)**/ Design & Analysis of VLSI Sub System **(23D57203D)**	Hardware Software Co Design **(23D55203)**	Introduction to Industry 4.0 and Industrial Internet of Things **(23D58203)**	Computer Vision **(23D13201)**
16-11-2026 Monday	Low Power VLSI Design **(23D57206)**	Digital Image Processing **(23D55207)**	Foundations of Cryptography **(23D58206)**	Reinforcement Learning **(23D13203)**

**** The Supplementary External Laboratory Examinations are scheduled form 17-11-2026 to 21-11-2026.**

C. Rao 3/11/26

ACE-3

Date: 03-11-2026

05/11/26

Controller of Examinations

PRINCIPAL