



SANTHIRAM ENGINEERING COLLEGE :: NANDYAL

(Autonomous)

EXAMINATION BRANCH

M.TECH I SEM (R25) Autonomous Supplementary Examination Timetable-June- 2026
(2025 Batch)

FN: 10:00 AM to 01:00 PM

Date/Day	VLSI System Design	Embedded Systems	Computer Science and Engineering	Computer Science and Engineering (AI&ML)
15-06-2026 Monday	CMOS Analog IC Design (25D57101)	Embedded System Design (25D55101)	Advanced Data Structures and Algorithms (25D58101)	Advanced Algorithms (25D13101)
16-06-2026 Tuesday	CMOS Digital IC Design (25D57102)	Embedded Programming (25D55102)	Distribute Operating Systems (25D58102)	Mathematics for AI and ML (25D13102)
17-06-2026 Wednesday	Research Methodology and IPR (25D57107) Common to All			
18-06-2026 Thursday	CAD for VLSI (25D57103A)	Wireless Sensor Networks (25D55103A)	Artificial Intelligence – Knowledge Representation and Reasoning (25D58103E)	Reinforcement Learning (25D13103D)
19-06-2026 Friday	FPGA Architectures and Applications (25D57104B)	Embedded Systems for Automobile Applications (25D55104C)	Cryptography and Networks Security (25D58104E)	Machine Learning & Deep Learning Applications (25D13104D)

**** The Supplementary External Laboratory Examinations are Scheduled from 23-06-2026 to 24-06-2026.**

C. Jayi
21/6/26
ACE-3

Date: 02-06-2026

K. G. Me
02/06/26
Controller of Examinations

[Signature]
PRINCIPAL
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Santhiram Engineering College
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