SANTHIRAM ENGINEERING COLLEGE :: NANDYAL
(Autonomous)

EXAMINATION BRANCH

MTECH I SEM (R23) Autonomous Supplementary Examination Timetable- January/February- 2026 (2023 & 2024 Batch's)

FN: 10:00 AM to 01:00 PM

Date/Day	VLSI System Design	Embedded Systems	Computer Science and Engineering	Computer Science and Engineering (AI&ML)
27-01-2026 Tuesday	FPGA Architectures and Applications (23D57107)		Machine Learning (23D13102)	
29-01-2026 Thursday	Research Methodology and IPR (23D57111)			
31-01-2026 Saturday	CAD for VLSI (23D57105)	Data Structures and Algorithms for Embedded Programming (23D55105)	Advanced Computer Networks (23D58102)	Advanced Database System (23D13103)
02-02-2026 Monday	CMOS Analog IC Design (23D57101)	Embedded System Design (23D55101)	Data Analytics Using R (23D58106)	Artificial Intelligence - Principles and Techniques (23D13101)
04-02-2026 Wednesday	CMOS Digital IC Design (23D57102)	Embedded Programming (23D55102)	Advanced Data Structures and Algorithms (23D58101)	

** The Supplementary External Laboratory Examinations are scheduled from 05-02-2026 to 07-02-2026

ACE-3
Date: 19-01-2026

Controller of Examinations

PRINCIPAL